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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No	09/332,271
Filing Date	June 11, 1999
Inventor	. Klaus Florian Schuegraf et al.
Assignee	Micron Technology, Inc.
Group Art Unit	2812
Examiner	Ron E. Pompey
Eattorney's Docket No	MI22-532
Titla: Methods for Forming Wordlines, Transistor Gates	, and Conductive Interconnects,
Title: Methods for Forming Wordlines, Transistor Gates and Wordline, Transistor Gate, and Conductive Ir	nterconnect Structures
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## SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR §1.56. No admission is made regarding whether any of the submitted references is prior art. Copies of the references are attached.

Respectfully Submitted:

Dated: 5-6-82

D. Brent Kenady Reg. No. 40,045

05/16/2002 CNGUYEN 00000030 09322271 01 FC:126 180.00 OP



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Sheet 1 of 1 Form PTO-1449 U.S. DEPARTMENT OF COMMERCE ATTY. DOCKET NO. SERIAL NO. PATENT AND TRADEMARK OFFICE M122-532 09/332,271 LIST OF ART CITED BY APPLICANT APPLICANT (Use several sheets if necessary) Klaus Florian Schuegraf FILING DATE GROUP June 11, 1999 2812 U.S. PATENT DOCUMENTS Name Class Subclass Filing Date Number If Appropriate AA 5,472,896 12/95 Chen et al. 437 44 ΑB 5,393,685 437 02/95 Yoo et al. 44 АC 4,833,099 05/89 Woo 437 41 5,915,197 AD 06/99 Yamanaka et al. 438 586 ΑE CLERCIE CHE aldastilled ΑF AG ΑH ΑI AJ AK - POTAL AND THE THE STATE OF THE AL FOREIGN PATENT DOCUMENTS Date Document Country Class Subclass Translation Number Yes No AM AN AO ΑP AQ OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.) AR AS ΑT **EXAMINER** DATE CONSIDERED

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.